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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/825,713	04/14/2004	Kuang-Yu Wang	SUP-005	9208
22888	7590	03/22/2007		
BEVER HOFFMAN & HARMS, LLP			EXAMINER	
TRI-VALLEY OFFICE			CARPIO, IVAN HERNAN	
1432 CONCANNON BLVD., BLDG. G				
LIVERMORE, CA 94550			ART UNIT	PAPER NUMBER

2841

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	03/22/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Office Action Summary

Application No.

10/825,713

Applicant(s)

WANG ET AL.

Examiner

Ivan H. Carpio

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 16 January 2007.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-32 is/are pending in the application.
- 4a) Of the above claim(s) 6-8, 11, 13-19, 22, 25-27, 29, 31 and 32 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-5, 9, 10, 12, 20, 21, 23, 24, 28 and 30 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 14 April 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) * | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Election/Restrictions

Applicant's election without traverse of Species 1 in the reply filed on 01/16/07 is acknowledged. Claims 6-8,11,13-19,25-27,29,31 and 32 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected invention, there being no allowable generic or linking claim. Election was made **without** traverse in the reply filed on 01/16/07.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5,9,10,12,20,21,23,24^{,28,} and 30 are rejected under 35 U.S.C. 102(b) as *PA 3/19/07*
being anticipated by Simmons (US 5339222).

With respect to claim 1 Simmons teaches a card-type (Fig. 1) electronic apparatus comprising: a printed circuit board assembly (Fig. 7, element 20); an upper cover (Fig. 7, element 12) comprising a plurality of upper sidewalls extending substantially perpendicularly from a top exterior surface of the upper cover, each of the plurality of upper sidewalls including an upper mating feature (Fig. 7, element 60); and a lower cover (Fig. 7, element 16) comprising a plurality of lower mating (Fig. 6, element 46) features and at least two lower sidewalls, the at least two lower sidewalls extending

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substantially perpendicularly from a bottom exterior surface of the lower cover, the plurality of lower mating features being inboard of the at least two lower sidewalls, wherein the plurality of upper mating features are joined to the plurality of lower mating features, and wherein the upper cover and the lower cover enclose the PCBA.

With respect to claim 2 and with all the limitations of claim 1, Simmons teaches that at least two lower sidewalls completely surround the upper cover (Fig. 1 and 7).

With respect to claim 3 and with all the limitations of claim 1, Simmons teaches that the at least two lower sidewalls partially surround the upper cover (Fig. 1 and 7).

With respect to claim 4 and with all the limitations of claim 1, Simmons teaches that the at least two lower sidewalls form a slip fit (Column 4, lines 5-9) with the upper cover.

With respect to claim 5 and with all the limitations of claim 1, Simmons teaches that the at least two lower sidewalls form an interference fit (Fig. 7) with the upper cover.

With respect to claim 9 and with all the limitations of claim 1, Simmons teaches the PCBA comprises a one or more integrated circuits (Note it's a PC Card and therefore has integrated circuits) mounted on a PCB, and wherein the lower cover comprises one or more support structures (Fig. 7, element 70), the one or more support structures being in contact with one or more unpopulated regions on the PCB.

With respect to claim 10 and with all the limitations of claim 1, Simmons teaches that each of the at least two lower sidewalls includes a top surface (Fig. 6, element 16, the lip edge), and wherein an upper external surface of the upper cover is above a

plane defined by the top surfaces of each of the at least two lower sidewalls (Fig. 7, element 12).

With respect to claim 12 and with all the limitations of claim 1, Simmons teaches that the card-type electronic apparatus comprises a memory stick (column 2, lines 28-30).

With respect to claim 20 Simmons teaches a card-type electronic (Fig. 1) apparatus comprising: a printed circuit board assembly (Fig. 7, element 20)); an upper cover (Fig. 7, element 12); and a lower cover (Fig. 7, element 16), the lower cover comprising a plurality of lower sidewalls extending substantially perpendicularly from a bottom exterior surface of the lower cover, the plurality of lower sidewalls defining a pocket, wherein the upper cover is attached to the lower cover at a seam (Fig. 6, element 46) within the pocket, wherein the upper cover and the lower cover enclose the PCBA.

Claim 21 is rejected with the same logic as claim 2.

With respect to claim 23 and with all the limitations of claim 20, Simmons teaches that the lower cover includes a plurality of support structures within the pocket, the plurality of support structures being in contact (Fig. 7, element 70) with unpopulated regions of the PCBA.

With respect to claim 24 and with all the limitations of claim 20, Simmons teaches that the upper cover comprises a plurality of upper sidewalls (Fig. 7, the sidewalls of element 12) extending substantially perpendicularly from an upper exterior surface of

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the upper cover, and wherein the plurality of upper sidewalls are positioned within the pocket (Fig. 7).

Claim 28 is rejected with the same logic as claim 10.

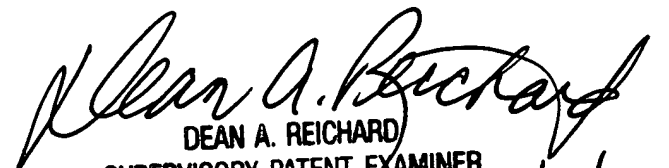
Claim 30 is rejected with the same logic as claim 12.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ivan H. Carpio whose telephone number is 571-272-8396. The examiner can normally be reached on T-F 7:00am - 5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean Reichard can be reached on 571-272-1984. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.


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SUPERVISORY PATENT EXAMINER
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3/19/07